

**Amendments to the Specification:**

Please replace the paragraph at lines 14-21 on page 7 with the following:

Low jitter buffer 1 410 receives an input ~~1~~ signal 1 411. Low jitter buffer 1 410 outputs an output ~~1~~ signal 415 1. An output signal 1 415 is received by low jitter buffer 2 420 as input signal 2 421 and by low jitter buffer 4 440 as input signal 4 441. Low jitter buffer 2 420 outputs output signal 2 425 and low jitter buffer 4 440 outputs output signal 4 445. Output signal 2 425 is input into low jitter buffer 3 430 as input signal 3 431. Low jitter buffer 3 430 then outputs output signal 3 ~~453~~ 435. It will be understood that cascaded symmetric voltage follower buffer circuit 400 is merely illustrative and not an accurate representation of the number of buffer stages utilized in the transmission of clocks or signals.

Please replace the paragraph at lines 7-12 on page 14 with the following:

A machine-readable medium includes any mechanism for storing or transmitting information in a form readable by a machine (e.g., a computer). For example, a machine-readable medium includes read only memory (ROM); random access memory (RAM); magnetic disk storage media; optical storage media; flash memory devices; electrical, optical, acoustical or other form of propagated signals (e.g., carrier waves, infrared signals, digital signals, etc.); etc. Instructions stored on a machine-readable medium may be performed by a set of processors.